



temp.wsp:1

- ☐ Drafts
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 - ☒ L1: (146372) erase or erasing or erased or erased
 - ☒ L2: (2941882) block or segment or subarray or (sub
 - ☒ L3: (948161) pulse
 - ☒ L4: (1739340) sequen\$6
 - ☒ L5: (1883704) reading or read or reads
 - ☒ L6: (1077) 4 adj4 5 adj8 2
 - ☒ L7: (5709) 1 adj5 2
 - ☒ L8: (40) 6 same 7
 - ☒ L9: (16137) 1.clm.
 - ☒ L10: (270204) 2.clm.
 - ☒ L11: (125492) 3.clm.
 - ☒ L12: (313474) 4.clm.
 - ☒ L13: (188667) 5.clm.
 - ☒ L14: (156) 12 adj4 13 adj8 10
 - ☒ L15: (868) 9 adj5 10
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	U	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval C
1	<input type="checkbox"/>	US 20040062082	20040401	11	Non-volatile memory device w	365/185.33	365/185.29	Ro:
2	<input type="checkbox"/>	US 20040057306	20040325	11	Non-volatile memory device w	365/200		Ro:
3	<input type="checkbox"/>	US 20030067805	20030410	11	Non-volatile memory device w	365/185.22		Ro:
4	<input type="checkbox"/>	US 20030031057	20030213	11	Non-volatile memory device w	365/185.33		Ro:
5	<input type="checkbox"/>	US 20020126539	20020912	13	Non-volatile memory device w	365/185.33		Ro:
6	<input type="checkbox"/>	US 6862222 B2	20050301	11	Non-volatile memory device w	365/185.29	365/185.11;	Ro:
7	<input type="checkbox"/>	US 6654289 B2	20031125	11	Non-volatile memory device w	365/185.22	365/185.11;	Ro:

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Inventor Name Search Result

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Your Search was:

Last Name = ROOHPARVAR

First Name = FRANKIE F.

Application#	Patent#	Status	Date Filed	Title	Inventor Name
08386688	5682496	150	02/10/1995	FILTERED SERIAL EVENT CONTROLLED COMMAND PORT FOR FLASH MEMORY	ROOHPARVAR, FRANKIE F.
08386704	5526364	150	02/10/1995	APPARATUS FOR ENTERING AND EXECUTING TEST MODE OPERATIONS FOR MEMORY	ROOHPARVAR, FRANKIE F.
08387018	5627786	150	02/10/1995	PARALLEL PROCESSING REDUNDANCY SCHEME FOR FASTER ACCESS TIMES AND LOWER DIE AREA	ROOHPARVAR, FRANKIE F.
08492781	Not Issued	162	06/21/1995	STABLE FREQUENCY OSCILLATOR HAVING TWO CAPACITORS THAT ARE ALTERNATELY CHARGED AND DISCHARGE	ROOHPARVAR, FRANKIE F.
08493162	5723990	150	06/21/1995	INTEGRATED CIRCUIT HAVING HIGH VOLTAGE DETECTION CIRCUIT	ROOHPARVAR, FRANKIE F.
08493166	5668483	150	06/21/1995	CMOS BUFFER HAVING STABLE THRESHOLD VOLTAGE	ROOHPARVAR, FRANKIE F.
08499025	5663908	150	07/06/1995	DATA INPUT/OUTPUT CIRCUIT FOR PERFORMING HIGH SPEED MEMORY DATA READ OPERATION	ROOHPARVAR, FRANKIE F.
08506168	5590089	150	07/25/1995	ADDRESS TRANSITION DETECTION (ATD) CIRCUIT	ROOHPARVAR, FRANKIE F.
08506970	5557576	150	07/28/1995	METHOD AND APPARATUS FOR MONITORING ILLEGAL CONDITIONS IN A NONVOLATILE MEMORY CIRCUIT	ROOHPARVAR, FRANKIE F.
08508797	5751944	150	07/28/1995	NON-VOLATILE MEMORY SYSTEM HAVING AUTOMATIC CYCLING TEST FUNCTION	ROOHPARVAR, FRANKIE F.
08508828	5801985	150	07/28/1995	MEMORY SYSTEM HAVING PROGRAMMABLE CONTROL PARAMETERS	ROOHPARVAR, FRANKIE F.
08508864	Not Issued	166	07/28/1995	NON-VOLATILE DATA STORAGE UNIT AND METHOD OF CONTROLLING SAME	ROOHPARVAR, FRANKIE F.
08508921	5619453	150	07/28/1995	MEMORY SYSTEM HAVING PROGRAMMABLE FLOW CONTROL REGISTER	ROOHPARVAR, FRANKIE F.
08508923	5627784	150	07/28/1995	MEMORY SYSTEM HAVING NON-VOLATILE DATA STORAGE STRUCTURE FOR MEMORY CONTROL PARAMETERS AND METHOD	ROOHPARVAR, FRANKIE F.
08508924	5619461	150	07/28/1995	MEMORY SYSTEM HAVING INTERNAL STATE MONITORING CIRCUIT	ROOHPARVAR, FRANKIE F.
08509021	5581206	150	07/28/1995	POWER LEVEL DETECTION CIRCUIT	ROOHPARVAR, FRANKIE F.
08509876	5617350	150	08/01/1995	FLASH MEMORY SYSTEM HAVING REDUCED DISTURB AND METHOD	ROOHPARVAR, FRANKIE F.
08511614	Not Issued	166	08/04/1995	MEMORY CIRCUIT FOR PERFORMING THERESHOLD VOLTAGE TESTS ON CELLS OF A MEMORY ARRAY	ROOHPARVAR, FRANKIE F.

<u>08563505</u>	<u>5615159</u>	150	11/28/1995	MEMORY SYSTEM WITH NON-VOLATILE DATA STORAGE UNIT AND METHOD OF INITIALIZING SAME	ROOHPARVAR, FRANKIE F.
<u>08567623</u>	<u>5636166</u>	150	12/05/1995	APPARATUS FOR EXTERNALLY TIMING HIGH VOLTAGE CYCLES OF NON-VOLATILE MEMORY SYSTEM	ROOHPARVAR, FRANKIE F.
<u>08589754</u>	<u>5825782</u>	150	01/22/1996	NON-VOLATILE MEMORY SYSTEM INCLUDING APPARATUS FOR TESTING MEMORY ELEMENTS BY WRITING AND VERIFYING DATA PATTERNS	ROOHPARVAR, FRANKIE F.
<u>08589823</u>	<u>5675540</u>	250	01/22/1996	NON-VOLATILE MEMORY SYSTEM HAVING INTERNAL DATA VERIFICATION TEST MODE	ROOHPARVAR, FRANKIE F.
<u>08592228</u>	<u>5793775</u>	150	01/26/1996	LOW VOLTAGE TEST MODE OPERATION ENABLE SCHEME WITH HARDWARE SAFEGUARD	ROOHPARVAR, FRANKIE F.
<u>08668398</u>	<u>5682345</u>	150	06/25/1996	NON-VOLATILE DATA STORAGE UNIT AND METHOD OF CONTROLLING SAME	ROOHPARVAR, FRANKIE F.
<u>08671717</u>	<u>5650963</u>	150	06/28/1996	METHOD AND APPARATUS FOR MONITORING ILLEGAL CONDITIONS IN A NONVOLATILE MEMORY CIRCUIT	ROOHPARVAR, FRANKIE F.
<u>08673119</u>	Not Issued	166	07/01/1996	REFERENCE BASED STABLE FREQUENCY OSCILLATOR	ROOHPARVAR, FRANKIE F.
<u>08717702</u>	<u>5767711</u>	150	09/23/1996	LEVEL DETECTION CIRCUIT AND METHOD	ROOHPARVAR, FRANKIE F.
<u>08731406</u>	<u>5864569</u>	150	10/18/1996	METHOD AND APPARATUS FOR PERFORMING ERROR CORRECTION ON DATA READ FROM A MULTISTATE MEMORY	ROOHPARVAR, FRANKIE F.
<u>08738541</u>	<u>5682355</u>	150	10/28/1996	ADDRESS TRANSITION DETECTION (ATD) CIRCUIT	ROOHPARVAR, FRANKIE F.
<u>08751072</u>	<u>5677885</u>	150	11/15/1996	MEMORY SYSTEM WITH NON-VOLATILE DATA STORAGE UNIT AND METHOD OF INITIALIZING SAME	ROOHPARVAR, FRANKIE F.
<u>08780624</u>	<u>5943263</u>	150	01/08/1997	APPARATUS AND METHOD FOR PROGRAMMING VOLTAGE PROTECTION IN A NON-VOLATILE MEMORY SYSTEM	ROOHPARVAR, FRANKIE F.
<u>08781427</u>	<u>5790459</u>	150	01/10/1997	MEMORY CIRCUIT FOR PERFORMING THRESHOLD VOLTAGE TESTS ON CELLS OF A MEMORY ARRAY	ROOHPARVAR, FRANKIE F.
<u>08789761</u>	<u>6067598</u>	150	01/28/1997	MEMORY SYSTEM HAVING PROGRAMMABLE FLOW CONTROL REGISTER	ROOHPARVAR, FRANKIE F.
<u>08789836</u>	<u>5933434</u>	150	01/28/1997	MEMORY SYSTEM HAVING INTERNAL STATE MONITORING CIRCUIT	ROOHPARVAR, FRANKIE F.
<u>08814220</u>	<u>5825700</u>	150	03/11/1997	LOW VOLTAGE TEST MODE OPERATION ENABLE SCHEME WITH HARDWARE SAFEGUARD	ROOHPARVAR, FRANKIE F.
<u>08814965</u>	<u>5950145</u>	150	03/11/1997	LOW VOLTAGE TEST MODE OPERATION ENABLE SCHEME WITH HARDWARE SAFEGUARD	ROOHPARVAR, FRANKIE F.
<u>08815112</u>	<u>6028798</u>	150	03/11/1997	LOW VOLTAGE TEST MODE OPERATION ENABLE SCHEME WITH HARDWARE SAFEGUARD	ROOHPARVAR, FRANKIE F.
<u>08832947</u>	<u>5761130</u>	150	04/04/1997	DATA INPUT/OUTPUT CIRCUIT FOR PERFORMING HIGH SPEED MEMORY DATA READ OPERATION	ROOHPARVAR, FRANKIE F.
<u>08833315</u>	<u>5774401</u>	150	04/04/1997	DATA INPUT/OUTPUT CIRCUIT FOR PERFORMING HIGH SPEED MEMORY DATA READ OPERATION	ROOHPARVAR, FRANKIE F.
<u>08850582</u>	<u>5880996</u>	150	05/02/1997	MEMORY SYSTEM HAVING NON-VOLATILE DATA STORAGE STRUCTURE FOR MEMORY CONTROL PARAMETERS AND METHOD	ROOHPARVAR, FRANKIE F.

08867257	5901108	150	06/02/1997	APPARATUS FOR EXTERNALLY TIMING HIGH VOLTAGE CYCLES OF NON-VOLATILE MEMORY SYSTEM	ROOHPARVAR, FRANKIE F.
08918927	5760655	150	08/26/1997	STABLE FREQUENCY OSCILLATOR HAVING TWO CAPACITORS THAT ARE ALTERNATELY CHARGED AND DISCHARGED	ROOHPARVAR, FRANKIE F.
08929209	5900741	150	09/09/1997	CMOS BUFFER HAVING STABLE THRESHOLD VOLTAGE	ROOHPARVAR, FRANKIE F.
08958487	5864499	150	10/24/1997	NON-VOLATILE DATA STORAGE UNIT AND METHOD OF CONTROLLING SAME	ROOHPARVAR, FRANKIE F.
08991222	5930188	150	12/12/1997	MEMORY CIRCUIT FOR PERFORMING THRESHOLD VOLTAGE TESTS ON CELLS OF A MEMORY ARRAY	ROOHPARVAR, FRANKIE F.
09031900	6205058	150	02/27/1998	DATA INPUT/OUTPUT CIRCUIT FOR PERFORMING HIGH SPEED MEMORY DATA READ OPERATION	ROOHPARVAR, FRANKIE F.
09094752	6243839	150	06/15/1998	NON-VOLATILE MEMORY SYSTEM INCLUDING APPARATUS FOR TESTING MEMORY ELEMENTS BY WRITING AND VERIFYING DATA PATTERNS	ROOHPARVAR, FRANKIE F.
09094825	6046615	150	06/15/1998	LEVEL DETECTION CIRCUIT	ROOHPARVAR, FRANKIE F.
09097470	6256754	150	06/15/1998	MEMORY SYSTEM HAVING INTERNAL STATE MONITORING CIRCUIT	ROOHPARVAR, FRANKIE F.
09103844	6272586	150	06/24/1998	MEMORY SYSTEM HAVING PROGRAMMABLE CONTROL PARAMETERS	ROOHPARVAR, FRANKIE F.

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